



#7
1-22-02
Dwyer

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Takumi HASEGAWA

Appln. No.: 09/273,560

Confirmation No.: unknown

Filed: March 22, 1999

For: DELAY ANALYSIS SYSTEM

Group Art Unit: 2682

Examiner: D. To

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JAN 16 2002

Technology Center 2600

SUBMISSION OF FORMAL DRAWING

Commissioner for Patents
Washington, D.C. 20231

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JAN 18 2002

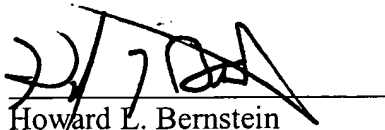
Technology Center 2100

Sir:

Submitted herewith please find 1 sheet (Fig. 1B) of formal drawing. The Examiner is respectfully requested to acknowledge receipt of these formal drawings.

The submitted drawing are believed to obviate the informalities indicated on Form PTO-948 attached to Paper No. 5.

Respectfully submitted,


Howard L. Bernstein
Registration No. 25,665

SUGHRUE MION, PLLC
2100 Pennsylvania Avenue, N.W.
Washington, D.C. 20037-3213
Telephone: (202) 293-7060
Facsimile: (202) 293-7860

Date: January 14, 2002

all to enter kx

APPROVED	O.G. FIG.	
BY	CLASS	SUBCLASS
DRAFTSMAN		

Inventor: Takumi HASEGAWA
Filing Date: March 22, 1999
Application No. 09/273,560
Howard L. Bernstein
Tel. (202) 293-7060
Fax (202) 293-7800

Sheet 1 of 1



FIG. 1(a)

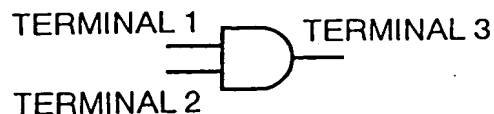


FIG. 1(b)

INPUT PIN	OUTPUT PIN	RISE/FALL OF INPUT	RISE/FALL OF OUTPUT	DELAY TIME (ns)
TERMINAL 1	TERMINAL 3	rise	rise	1
TERMINAL 1	TERMINAL 3	rise	fall	2
TERMINAL 1	TERMINAL 3	fall	rise	3
TERMINAL 1	TERMINAL 3	fall	fall	4
TERMINAL 2	TERMINAL 3	rise	rise	5
TERMINAL 2	TERMINAL 3	rise	fall	6
TERMINAL 2	TERMINAL 3	fall	rise	7
TERMINAL 2	TERMINAL 3	fall	fall	8

FIG. 1(c)

TERMINAL 1 \ TERMINAL 2	0	1
	0	1
0	0	0
1	0	1